

Formal drawings Approved

367.39268X00
PAT 99324US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Soren NORSKOV
Serial No.: 09/721,677
Filed: November 27, 2000
For: GROUND PLANE FOR A SEMICONDUCTOR CHIP
Art Unit: 2827
Examiner: Jose H. Alcala

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REQUEST FOR AUTHORIZATION TO SUBSTITUTE FORMAL DRAWINGS

Assistant Commissioner for Patents
Washington, D. C. 20231

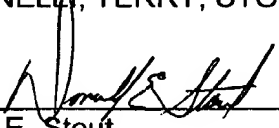
January 16, 2003

Sir:

The Examiner's permission is requested to substitute the attached Formal Drawings (non-bristol boards) of Figs. 1-4 for the original drawings of Figs. 1-4. The attached drawings include the changes requested to be made in the December 21, 2002 Request for Authorization to Amend the Drawings, the modification of Figs. 1 and 2 as requested by the Examiner in the Final Rejection, and addition of reference numeral 1' which has also been added to the specification to identify the first capacitor plate formed by the conductors within the chip 1. Early approval of the drawings is respectfully requested.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



Donald E. Stout
Registration No. 26,422
(703) 312-6600

Enclosures
DES:dlh